

1975 U.S. 09/625643 07/25/00		Class	Subclass	PATENT NUMBER	
U.S. UTILITY Patent Application					
		O.I.P.E.	R/2	PATENT DATE	
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APPLICANTS Noriaki Hiraga	APPLICATION NO. 09/625643	CONT/PRIOR F	CLASS 327 361	SUBCLASS 056,000	ART UNIT 2816 2836 Kikozu
TITLE Semiconductor integrated circuit device with enhanced resistance to electrostatic breakdown					

U.S. UTILITY Patent Application

O.I.P.E.

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APPLICATION NO.
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APPLICANTS

? Noriaki Hiraga

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Semiconductor electrostatic integrated circuit device with enhanced resistance to breakdown

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ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)				
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<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.		Sheets Drwg. 	Figs. Drwg. 	Print Fig. 	Total Claims 
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____.		Primary Examiner _____  (Assistant Examiner) _____  (Date) _____		NOTICE OF ALLOWANCE MAILED 	
<input checked="" type="checkbox"/> The terminal _____ months of this patent have been disclaimed.		Primary Examiner _____  (Legal Instruments Examiner) _____  (Date) _____		ISSUE FEE  Amount Due _____  Date Paid _____ 	
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